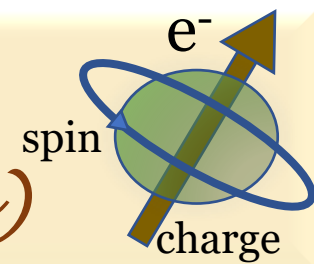




W2S Seminar

(Webinar series on Spintronics)



Magnetic tunnel junction: from nonvolatile memory to probabilistic computing

Speaker:

Prof. Hideo Ohno

Tohoku University, Sendai, Japan

Date and time:

28.10.2021 at

3.00 pm IST

i.e. 11.30 am CET

Abstract

Magnetic tunnel junction is the key device to realize ultra-low power integrated circuits because it can provide nonvolatile working memory. After reviewing the current status, I will first describe how one can scale magnetic tunnel junction down below 5 nm. I will then show that one can construct a probabilistic computer utilizing unstable magnetic tunnel junction to solve optimization problems.

To attend the lecture please visit: **Passcode:** xqTX7w Zoom link:
<https://us06web.zoom.us/j/95746360802?pwd=TjZLclpCcG43WUJYa2licW00eWNyUT09>

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